

REMARKS

Claims 1-15 remain in the application for consideration of the Examiner.

Claims 1 was objected to because of informalities.

By the instant amendment, Claim 1 has been amended to take into consideration the helpful comments of the Examiner.

It is respectfully submitted that Claims 1-15 are free from informalities.

Claims 1, 2, and 13-15 were rejected under 35 U.S.C. § 102(b) as being anticipated by Urakawa; and Claims 3-12 were rejected under 35 U.S.C. § 103 as being unpatentable over Urakawa.

These rejections are respectfully traversed.

It is respectfully submitted that Urakawa does not disclose or suggest the presently claimed invention including a differential first pair of transistor to input a differential signal and a current switch coupled to the second pair of nodes to steer current based on the polarity of the first pair of input nodes.

Urakawa discloses a single input A to transistor Q1. No differential signal is disclosed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is

respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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